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IN THE UNITED STATES PATENT AND TRADEMARK OFFICEApplicant: **Amnon Ganot et al.**

Group: 2825

Serial No.: 10/045,651

Examiner: Not Yet Assigned

Filing Date: November 7, 2001

For: **MULTI-LAYER PRINTED CIRCUIT BOARD
FABRICATION SYSTEM AND METHOD**

September 11, 2002

Commissioner for Patents
Washington, D.C. 20231**POWER TO INSPECT**

I hereby authorize Lisa Nelson or anyone else from Berlin & Associates to
review the file of the above application and to make any copies thereof.

Respectfully submitted,

REED SMITH LLP

September 11, 2002

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